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ISSUE CORRECTION	U.S. UTILITY Patent Application O.I.P.E. PATENT DATE
APPLICATION NO. CONT/PRIOR 09/531677 F	CLASS SUBCLASS ART UNIT 2734 EXAMINER WILL MAN 1438-375 376 2825-672 FAV. C.

APPLICANT Hiroshi Baba

Phase comparator and method of controlling power saving operation of the same, and semiconductor integrated circuit

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ISSUING CLASSIFICATION									
ORIGIN	AL			EFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
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TERMINAL		DRAWINGS	*	CLAIMS ALLOWED		
L_ DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
☐ The term of this patent		<u> </u>		NOTICE OF ALLOWANCE MAILED		
subsequent to(date) has been disclaimed.	LAWrence (Assistant	Examiner)	(Date)	1-14	Lnic	
The term of this patent shall not extend beyond the expiration date of U.S Patent. No	Ste	PHEN CHIN	634	ISSUE FEE		
	SUPERVISORY	PATENT EXAM By Center 28		Amount Due	Date Paid	
☐ The terminalmonths of this patent have been disclaimed.	LM	enta Exp(niner)	1-20 OH (Date)	ISSUE BA	TCH NUMBER	
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